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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF: Adolf Bernds et al.
SERIAL NO: 10/508,737 GROUP ART UNIT: Not assigned
FILED: September 21, 2004 EXAMINER: Not assigned
CUSTOMER NO.: 27162
FOR: Device and Method for Laser Structuring Functional Polymers and the Uses thereof
ATTY/DKT NO.: 411000-121

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

Pursuant to 37 C.F.R. §1.56(a), Applicant(s) hereby cite(s) the enclosed documents listed on the attached copy of Form PTO-1449 which are believed to be material to the patentability of the above-identified application.

This Information Disclosure Statement is filed in accordance with the paragraph of 37 CFR §1.97 indicated below:

X §1.97(b)

This Information Disclosure Statement is filed:

- (1) Within three months of the filing date of a national application; OR
- (2) Within three months of the date of entry of the national stage of an international application; OR
- (3) Before the mailing of a first Office Action on the merits. No fee or statement is required.

____ §1.97(c) This Information Disclosure Statement is filed after the period specified in paragraph (b) above, but before the mailing date of either:

- (1) A Final Action or under 37 CFR §1.113; OR
- (2) A Notice of Allowance under 37 CFR §1.311; AND

is accompanied by either: (check one)

____ The statement as specified in 37 CFR §1.97(e) set out below; OR

____ The fee of \$180.00 under 37 CFR §1.17(p).

___§1.97(d) This Information Disclosure Statement is filed after the mailing date of either:

- (1) a Final Action or under 37 CFR §1.113; OR
- (2) A Notice of Allowance under 37 CFR §1.311;

BUT filed on or before payment of the Issue Fee; AND
is accompanied by:

- (1) The statement as specified in 37 CFR §1.97(e) as set forth below; AND
- (2) Petition is hereby made under 37 CFR §1.97(d) for consideration of this Information Disclosure Statement; AND,
- (3) The petition fee of \$180.00 set out in 37 CFR §1.17(i).

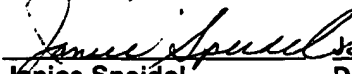
___§1.97(e) The undersigned Attorney hereby states that:

___ each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of this Information Disclosure Statement;
or

___ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, or to the knowledge of the undersigned Attorney after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing date of the Information Disclosure Statement.

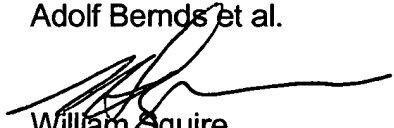
This Information Disclosure Statement provides a detailed list of the references cited in various pending applications listed in applicant's Disclosure Statement filed on December 1, 2004.

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

FIRST CLASS CERTIFICATE	
I hereby certify that this correspondence is being deposited today with the U.S. Postal Service as First Class Mail in an envelope addressed to:	
MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	
 Janice Speidel	Jan. 3, 2005 Date

#243206

Respectfully submitted,
Adolf Bernds et al.


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STATEMENT BY APPLICANT**

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Sheet

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Of

11

Application Number

10/508,737

Filing Date

September 21, 2004

First Named Inventor

Adolf Bernds

Group Art Unit

Not Assigned

Examiner Name

Not Assigned

Attorney Docket Number

411000-121

U.S. PATENT DOCUMENTS

Examiner Initial*	Cite No. ¹	Document Number Number-Kid Code ² (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-5,574,291	11/12/1996	Dodabalapur et al.	See IDS filed 9/30/2004 on all references cited
	2	US-5,206,525	04/27/1993	Yamamoto et al.	
	3	US-6,045,977	04/04/2000	Chandross et al.	
	4	US-5,854,139	12/29/1998	Kondo et al.	
	5	US-5,625,199	04/29/1997	Baumbach et al.	
	6	US-5,883,397	03/16/1999	Isoda et al.	
	7	US-5,998,805	12/07/1999	Shi et al.	
	8	US-6,384,804	05/07/2002	Dodabalapur et al.	
	9	US-6,072,716	06/06/2000	Jacobsen et al.	
	10	US-4,937,119	06/26/1990	Nickles et al.	
	11	US-5,705,826	01/06/1998	Aratani et al.	
	12	US-6,555,840	04/29/2003	Hudson et al.	
	13	US-6,197,663	03/06/2001	Chandross et al.	

FOREIGN PATENT DOCUMENTS

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	14	EU 0460242	12/11/1991			X
	15	WO 93 16491	08/19/1993			X
	16	WO 99 40631	08/12/1999			X
	17	WO 99 54936	10/28/1999			X
	18	EP 0981165	02/23/2000			X

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	19	US-6,221,553	04/24/2001	Wolk et al.	
	20	US-6,150,668	11/21/2000	Bao et al.	
	21	US-6,362,509	03/26/2002	Hart	
	22	US-6,329,226	12/11/2001	Jones et al.	
	23	US-6,593,690	07/15/2003	McCormick et al.	
	24	US-6,498,114	12/24/2002	Amundson et al.	
	25	US-6,603,139	08/05/2003	Tessler et al.	
	26	US-6,621,098	09/16/2003	Jackson et al.	
	27	US-4,340,657	07/20/1982	Rowe	
	28	US-5,364,735	11/15/1994	Akamatsu et al.	
	29	US-6,330,464	12/11/2001	Colvin et al.	
	30	US-5,691,089	11/25/1997	Smayling	
	31	US-6,321,571	11/27/2001	Themont et al.	

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	32	WO 99 30432	06/17/1999			X
	33	EP 0 108650	05/16/1984			X
	34	EP 0 418504	03/27/1991			X
	35	DE 198 51703	05/04/00			

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Filing Date	September 21, 2004
First Named Inventor	Adolf Berndt
Group Art Unit	Not Assigned
Examiner Name	Not Assigned
Attorney Docket Number	411000-121

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	37	US-6,087,196	07/11/2000	Sturm et al.	
	38	US-6,133,835	10/17/2000	De Leeuw et al.	
	39	US-5,970,318	10/19/1999	Choi et al.	
	40	US-5,321,240	06/14/1994	Takahira	
	41	US-5,973,598	10/26/1999	Beigel	
	42	US-5,347,144	09/13/1994	Garnier et al.	
	43	US-6,207,472	03/27/2001	Calligari et al.	
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	48	EP 0 786820	07/30/1997			X
	49	WO 99 39373	08/05/1999			X
	50	WO 02/47183	06/13/2002			X
	51	EP 1 103916 (title page only)	05/30/2001			
	52	EP 0 528662				
	53	EP 0 442123				

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	54	EP 0962984	12/08/99			X
	55	WO 02 19443	03/07/02			X
	56	WO 00 79617	12/28/04			X
	57	WO 99 10939	03/04/99			X
	58	DE 100 43204	04/04/2002			
	59	EP 0 511807	11/04/1992			X
	60	EP 0 979715	02/16/2000			
	61	EP 1 048 912	11/02/2000			
	62	DE 4243832	06/30/94			
	63	EP 0716458	06/12/1996			X
	64	WO 99 21233	04/29/1999			X
	65	WO 01 15233	03/01/2001			X
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Group Art Unit	Not Assigned
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Sheet 5

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		US-			
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	66	WO 98/40930	09/17/98			X
	67	DE 19933757	01/25/01			
	68	FR 2793089	11/03/2000			
	69	WO 01 47045	06/28/2001			X
	70	WO 94/17556	0804/1994			X
	71	WO 99/07189	02/11/1999			X
	72	WO 97/18944	05/29/1997			X
	73	EP 0685985	12/06/1995			X
	74	WO 00/36666	06/22/2000			X
	75	DE 100 12204 (title page)	09/20/2001			
	76	DE 198 16 860	11/18/99			
	77	WO 02/065557	08/22/2002			X

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	79	JP 05259434	10/05/1993			X
	80	WO 99/10929 (title page only)	03/04/1999			X
	81	DE 199 35 527	02/08/2001			
	82	DE 199 37 262	03/01/2001			
	83	WO 01/27998 (title page only)	04/19/2001			X
	84	JP 05152560	06/18/1993			X
	85					
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	87					

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Examiner Name	Not Assigned
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NON PATENT LITERATURE DOCUMENTS

Examiner Initial*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	88	GARNIER F et al.; "Vertical Devices Architecture By Molding Of Organic-Based Thin Film Transistor", Applied Physics Letters, American Institute Of Physics. XP000784120, issn: 0003-6951 abbildung 2	X
	89	COLLET J. et al.; "LOW VOLTAGE, 30 NM CHANNEL LENGTH, ORGANIC TRANSISTORS WITH A SELF-ASSEMBLED MONOLAYER AS GATE INSULATING FILMS", APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, Bd 76, Nr. 14, 3. april 2000 (2000-04-03), Seiten 1941-1943, XP000950589, ISSN:0003-6951, das ganze Dokument	X
	90	HWANG J D et al.; "A Vertical Submicron Sic thin film transistor", Solid State Electronics, Elsevier Science Publishers, Barking, GB, Bd. 38, NR. 2,1. February 1995 (1995-02-01), Seiten 275-278, XP004014040, ISSN:0038-1101, Abbildung 2	X
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Sheet

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Application Number

10/508,737

Filing Date

September 21, 2004

First Named Inventor

Adolf Bernds

Group Art Unit

Not Assigned

Examiner Name

Not Assigned

Attorney Docket Number

411000-121

NON PATENT LITERATURE DOCUMENTS

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	98	ZANGARA L: "Metall Statt Halbleiter, Programmierung Von Embedded ROMS Ueber Die Metallisierungen", Elektronik, Franzis Verlag GMBH, Munchen, DE, Bd. 47, Nr. 16, 4 August 1998 (1998-08-04), Seiten 52-55, XP000847917, ISSN: 0013-5658, Seite 52, rechtes Plate, Zeile 7-Seite 53, linke Spalte, Zeile 14; Abbildungen 1, 2	
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	101	PATENT ABSTRACTS OF JAPAN, Vol. 009, No. 274 (E-354), 31 Oktober 1985 (1985-10-31) & JP 60 117769 A (Fujitsu KK), 25 Juni 1985 (1985-06-25) Zusammenfassung	X
	102	ZIE VOOR TITEL BOEK, de 2e PAGINA, XP-002189001, PG 196-228.	X
	103	DRURY et al., "Low-Cost All-Polymer Integrated Circuits", American Institute of Physics, Applied Physics Letters, 1998, Vol. 73, No. 1, pp 108-110, July 6, 1998.	X
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	107	BAO, Z. et al., "High-Performance Plastic Transistors Fabricated by Printing Techniques", Chem. Mater Vol. 9, No. 6, 1997, pp 1299-1301.	X
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First Named Inventor	Adolf Berndt
Group Art Unit	Not Assigned
Examiner Name	Not Assigned
Attorney Docket Number	411000-121

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	118	GARNIER et al., "Conjugated Polymers and Oligomers as Active Material For Electronic Devices", Synthetic Metals, Vol. 28, 1989	X
	119	GELINCK, G.H. et al., "High-Performance All-Polymer Integrated Circuits", Applied Physics Letters, v. 77, 2000, pp. 1487-1489.	X
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	125	ULLMAN, A. et al., "High Performance Organic Field-Effect Transistors and Integrated Inverters", Mat. Res. Soc. Symp. Proc., v. 665, 2001, pp. 265-270.	X
	126	FIX, W. et al., "Fast Polymer Integrated Circuits Based on a Polyfluorene Derivative", ESSDERC 2002, 2002, pp. 527-529.	X
	127	KNOBLOCH, A. et al., "Printed Polymer Transistors", Proc. Polytronic, v. 84, 2001, pp. 84-89	X
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	129	CLEMENS, W. et al., "Vom Organischen Transistor Zum Plastik-Chip," Physik Journal, V. 2, 2003, pp. 31-36.	X

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	125	CRONE, B. ET AL, "Large-scale complementary Integrated circuits based on Organic transistors", Nature, Vol. 403, Feb. 3, 2000, PP. 521 -	X
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	129	BROWN, A.R. et al., "Field-effect transistors made from solution-processed organic semiconductors", Elsevier Science, S.A., Synthetic Metals 88 (1997) pp. 37-55	X
	130	BROWN, A.R., "Logic Gates Made from Polymer Transistors and Their Use in Ring Oscillators", Science, Vol. 270, November 10, 1995, pp 972 - 974	X
	131	KLAUK, H. et al., "Pentacene Thin Film Transistors and Inverter Circuits", 1997 International Electron Devices Meeting Technical Digest, pages 539-542, December 1997	X
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	133	BAO, Z. et al., "Organic and Polymeric Materials for the Fabrications of Thin Film Field-Effect Transistors", paper presented at the meeting of American Chemical Society, Division of Polymer Chemistry, XX, XX, Bd. 39, Nr. 1, 29 Marz 1998 (1998-03-29), P001032497, ISSN: 0032-3934 das ganze Dokument	X
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